

PATENT APPLICATION

Attorney Docket: 54364

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gupta, et al.
Serial No.: 09/847,667
Filed: 5/1/2001
For: Method for Bonding Wafers to Produce Stacked Integrated Circuits
Group Art Unit: 2827

Examiner: James Mitchell

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF RESPONSE TO NOTICE OF NON-COMPLIANCE

In response to the Notice of Non-compliance dated 4/8/04, Applicant re-submits herewith the amendment originally filed 4/1/04 in the above identified patent application. The listing of the claims now includes the text of the withdrawn claims as required.

Applicants believe that no fee is required. However, the Commissioner is hereby authorized to charge any fees which may be required in this application under 37 C.F.R. Section 1.16-1.17 during its entire pendency, or credit any overpayment, to Deposit Account No. 23-0424. This sheet is filed in duplicate.

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Calvin B. Ward

Registration No. 30,896
Date: 4/12/04

The Law Offices of Dr. Calvin B. Ward
18 Crow Canyon Court, Suite 305
San Ramon, CA 94583
(925) 855-0413
Fax (925) 855-9214

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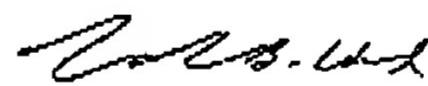
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AMENDMENT

Commissioner for Patents
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Sir:

In reply to the Office Action dated 12/4/2003, in the above-identified patent application, Applicant submits the following Amendments and Remarks. The period for response to this office action has been extended by one month by the accompanying petition for extension of time. Applicant respectfully requests that the following amendments be entered into the above-identified application and that the Examiner reconsider the grounds for rejection stated in the Office Action.